

FIG. 1
Local Area Network (LAN) Abstract View
(Prior Art)

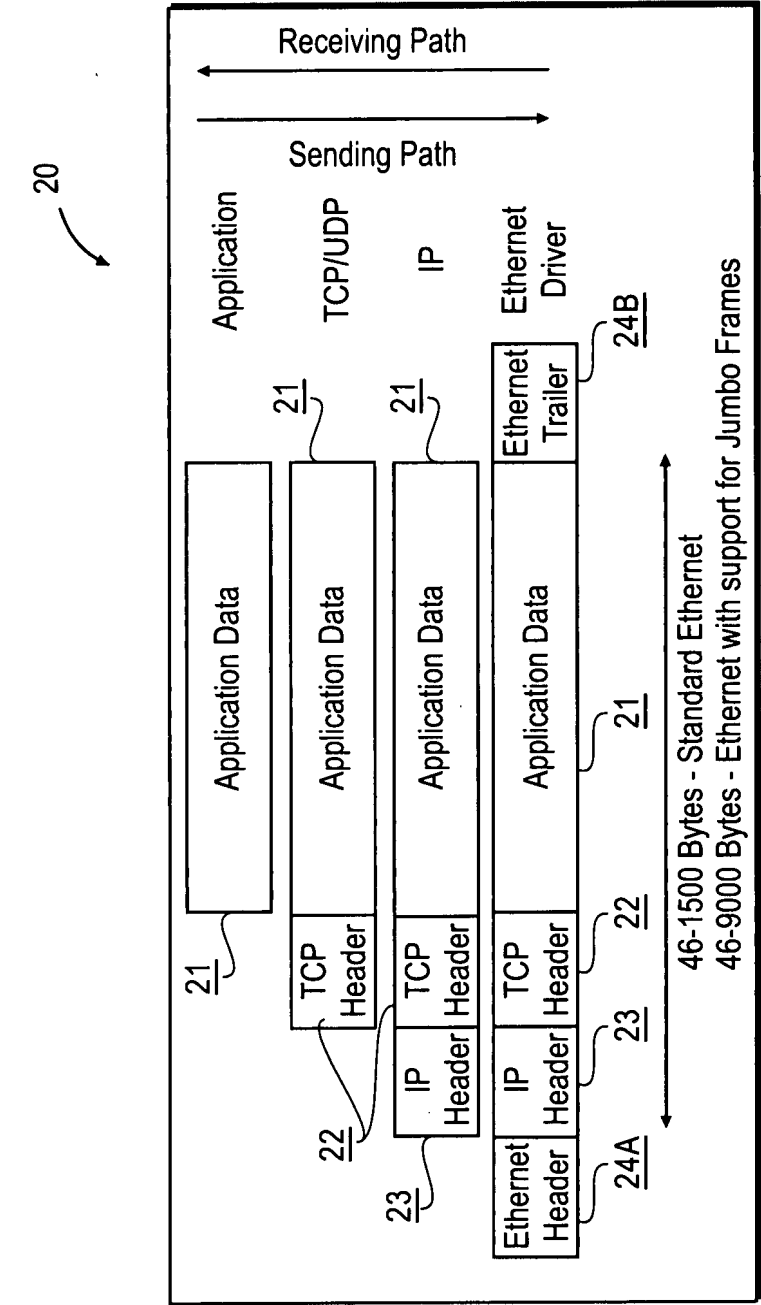


FIG. 2 Data Encapsulation Through The Network Stack
(Prior Art)

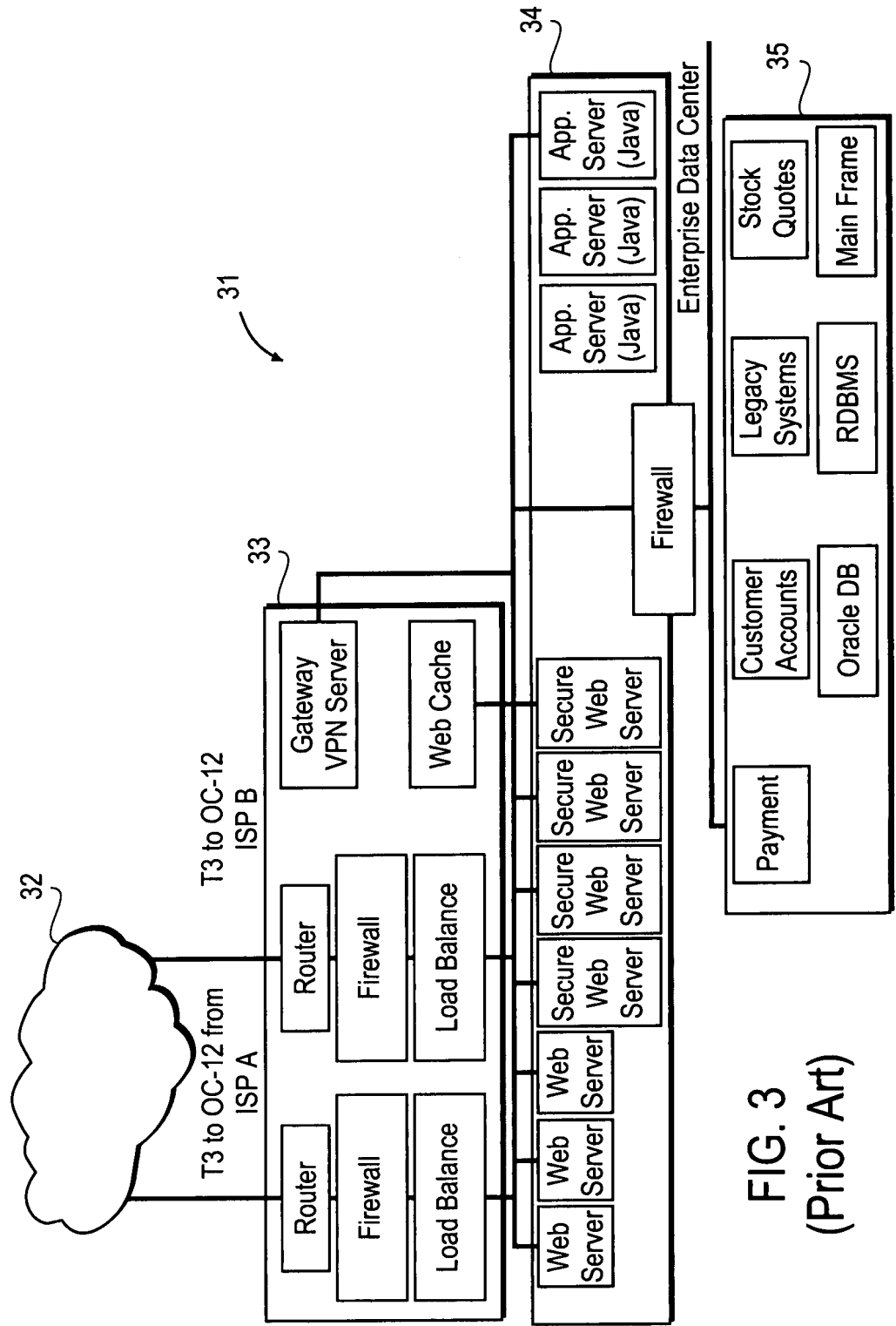


FIG. 3
(Prior Art)

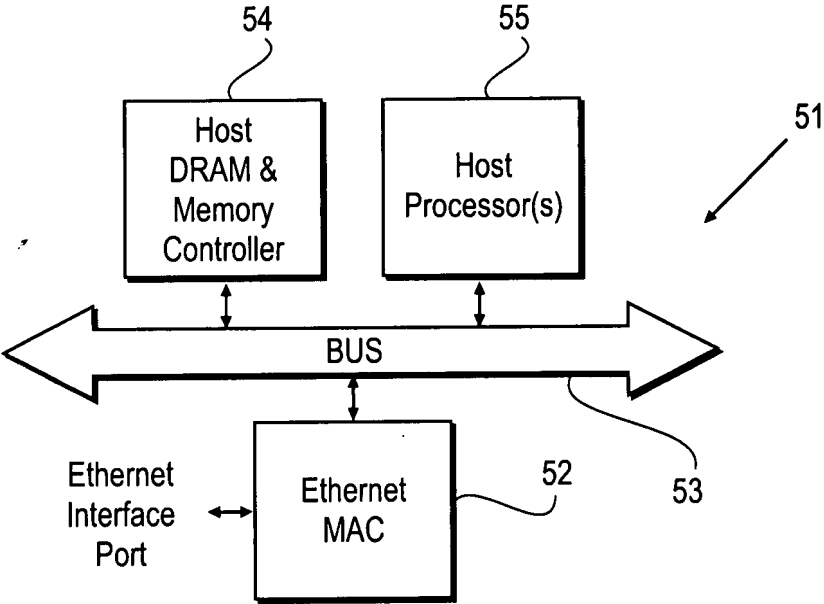


FIG. 4
(Prior Art)

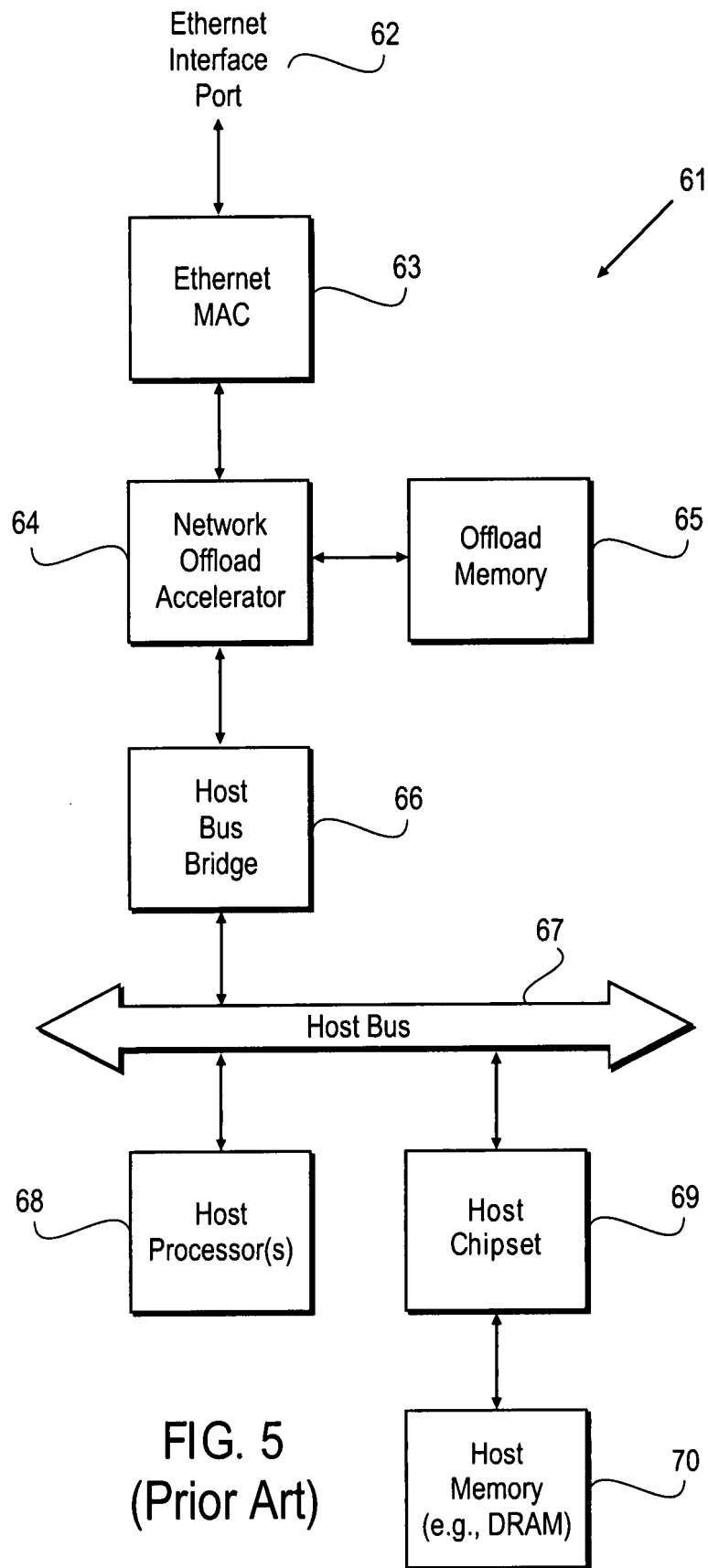


FIG. 5
(Prior Art)

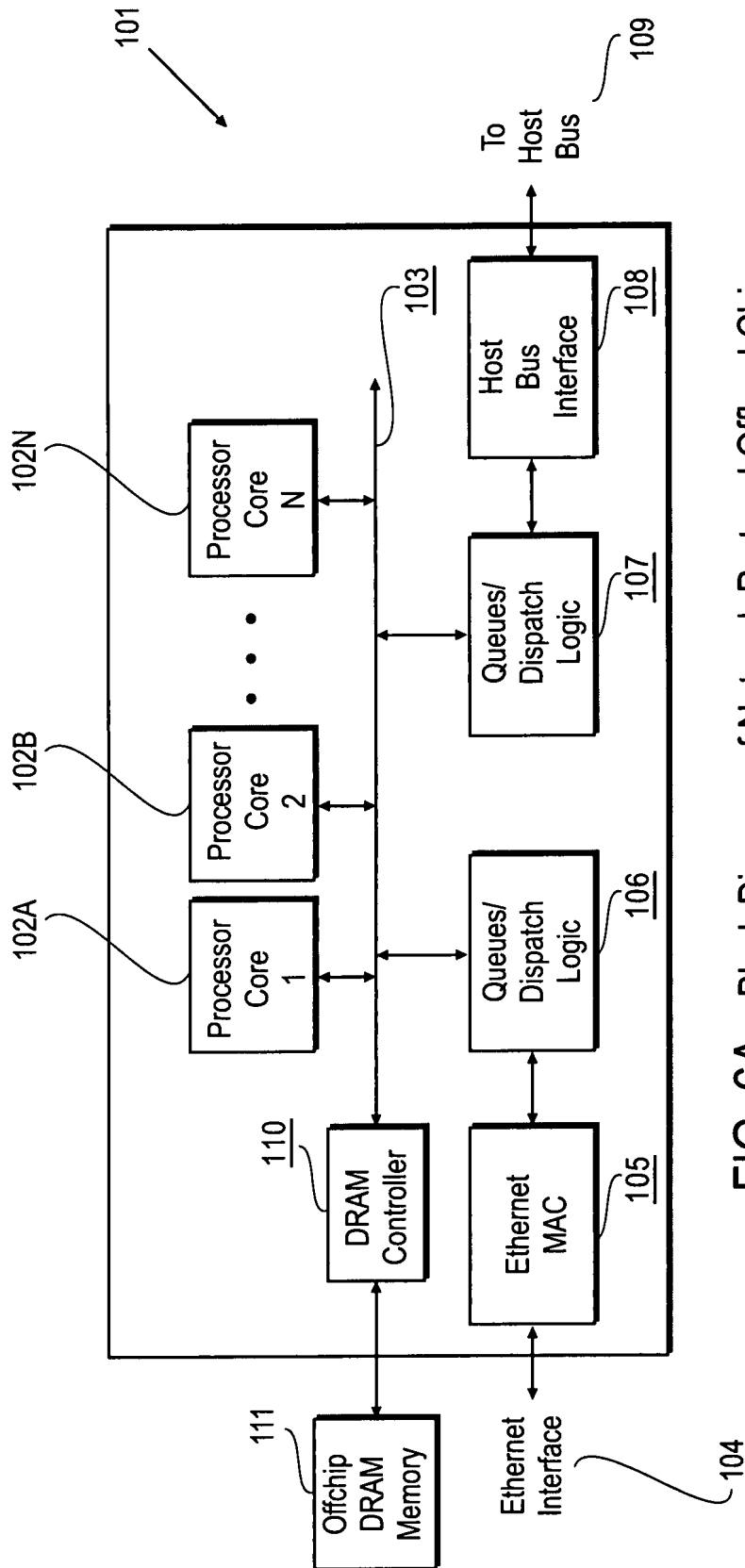


FIG. 6A Block Diagram of Network Protocol Offload Chip

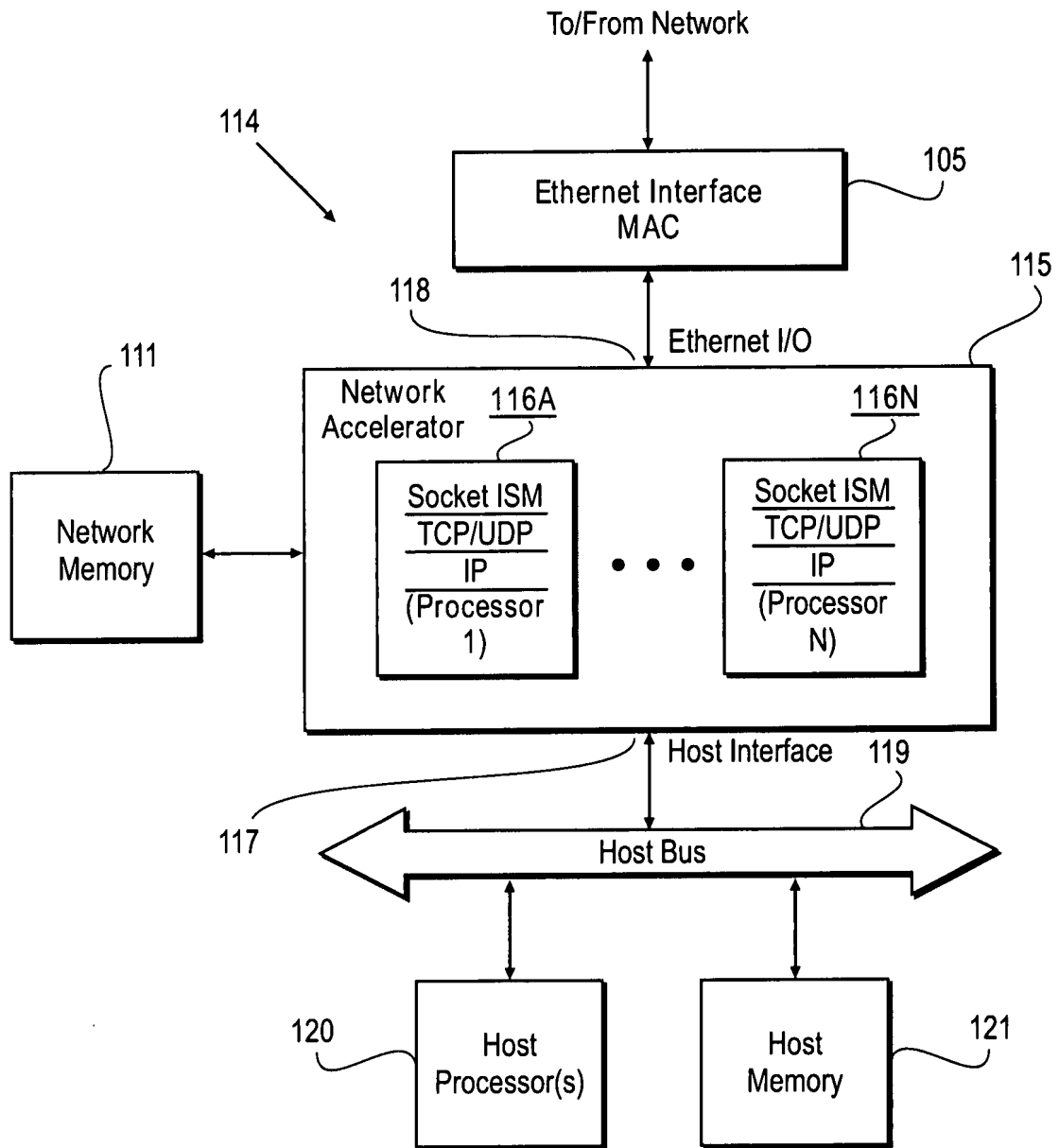


FIG. 6B

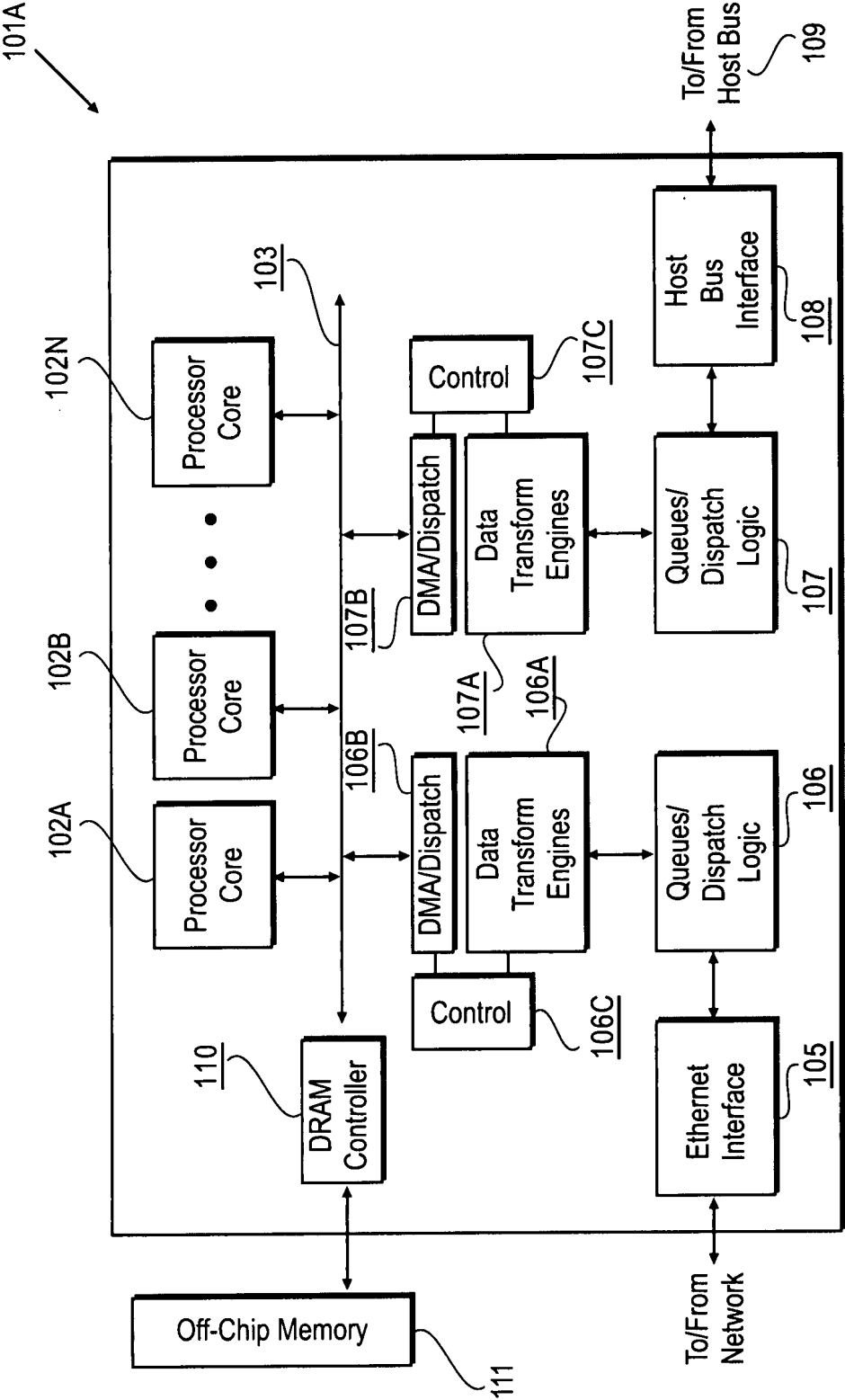


FIG. 6C Block Diagram of Data Transform Acceleration

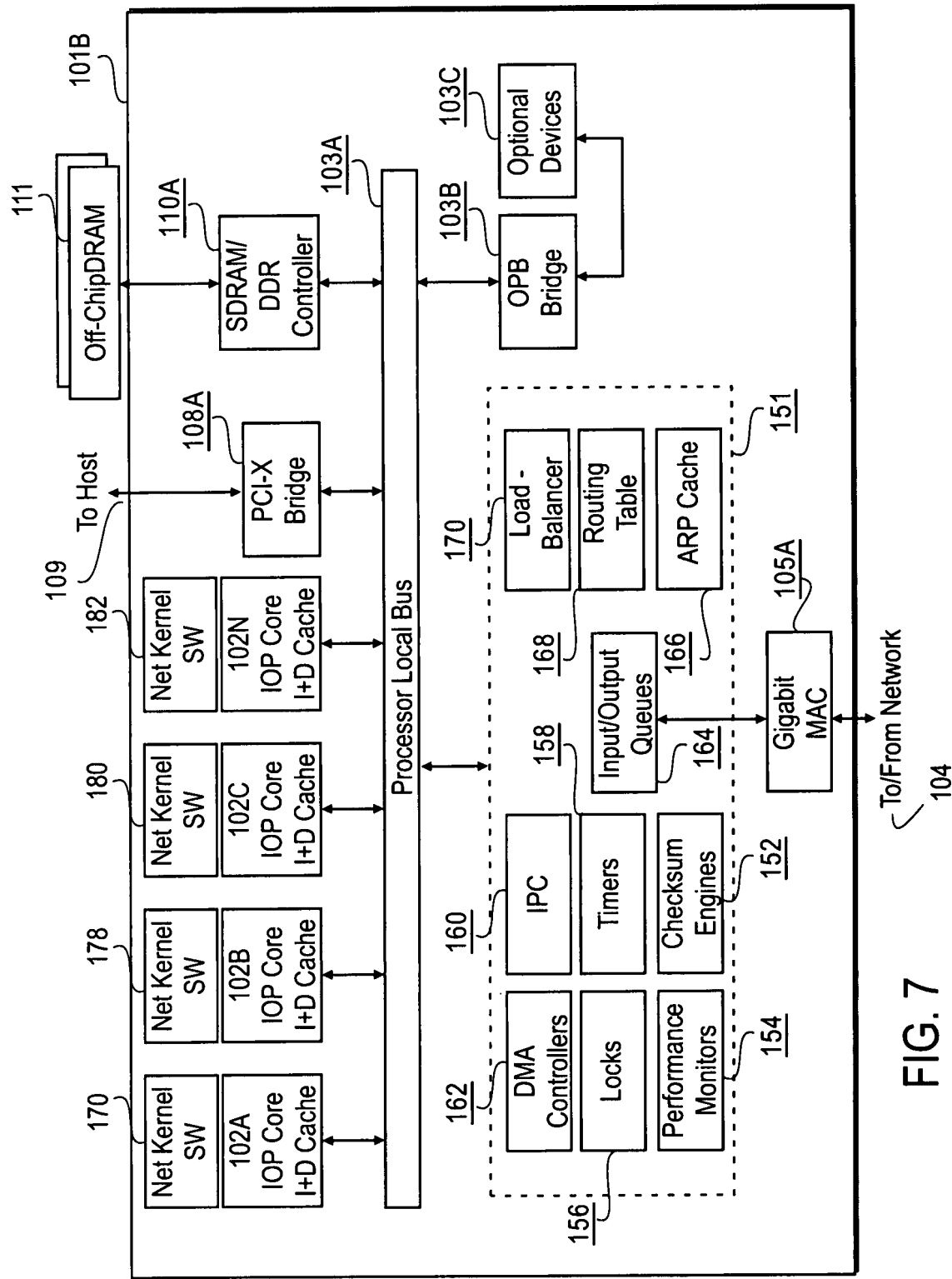


FIG. 7

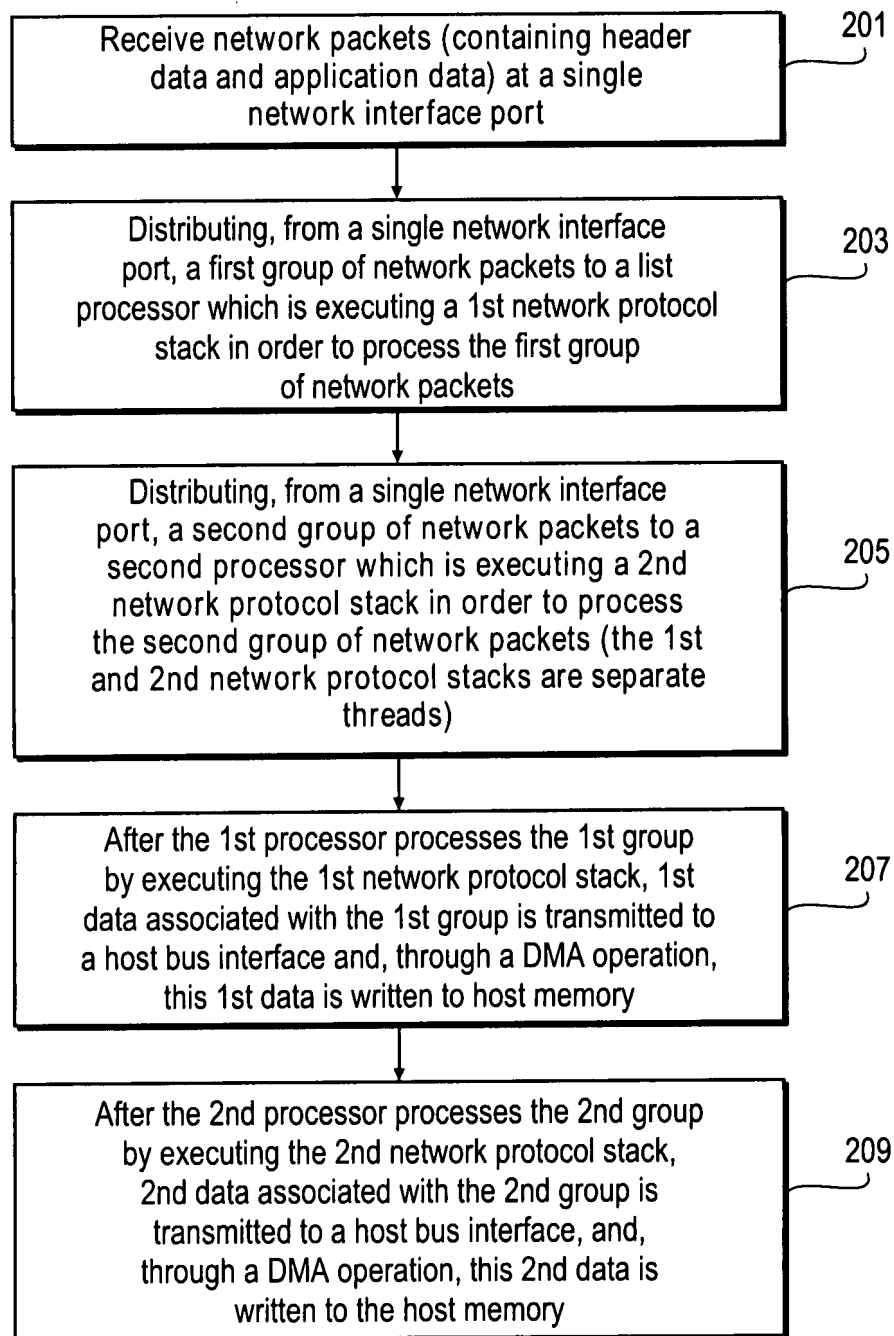


FIG. 8A

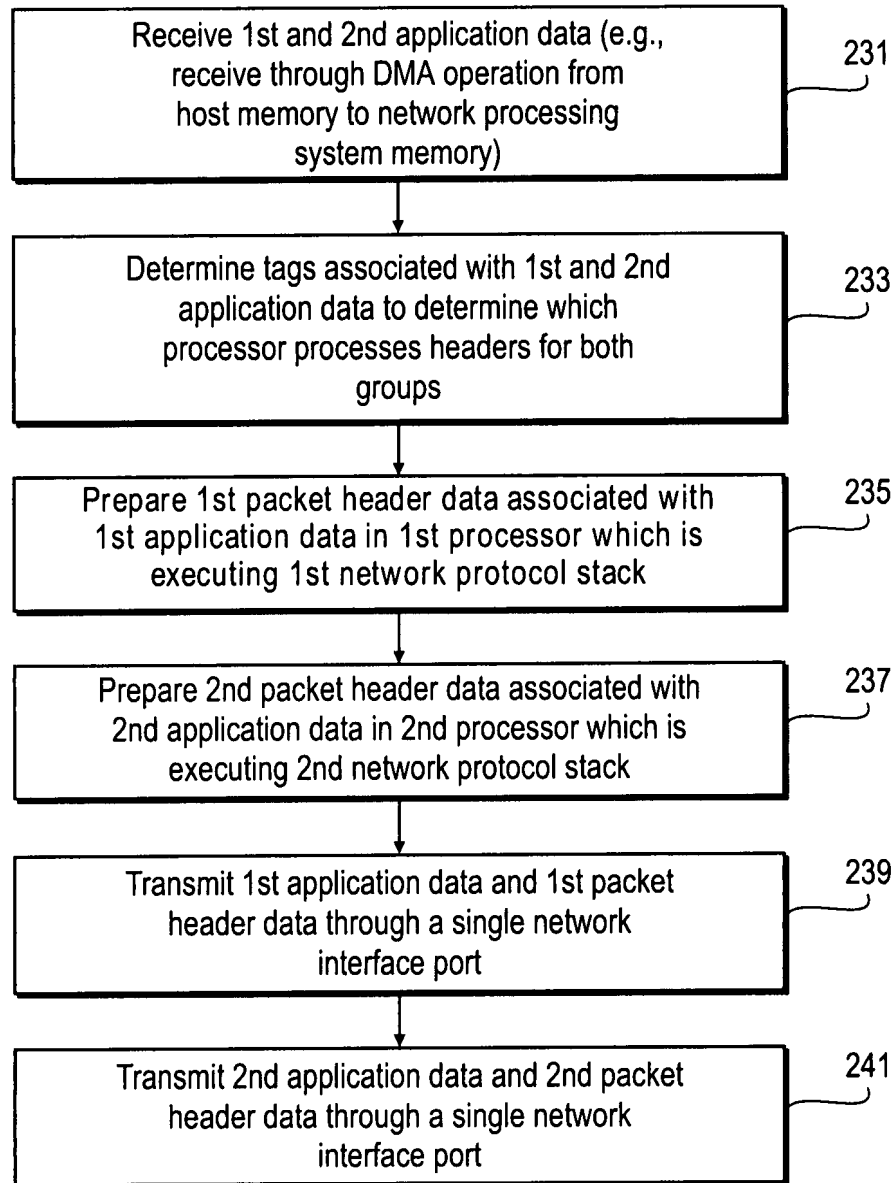


FIG. 8B

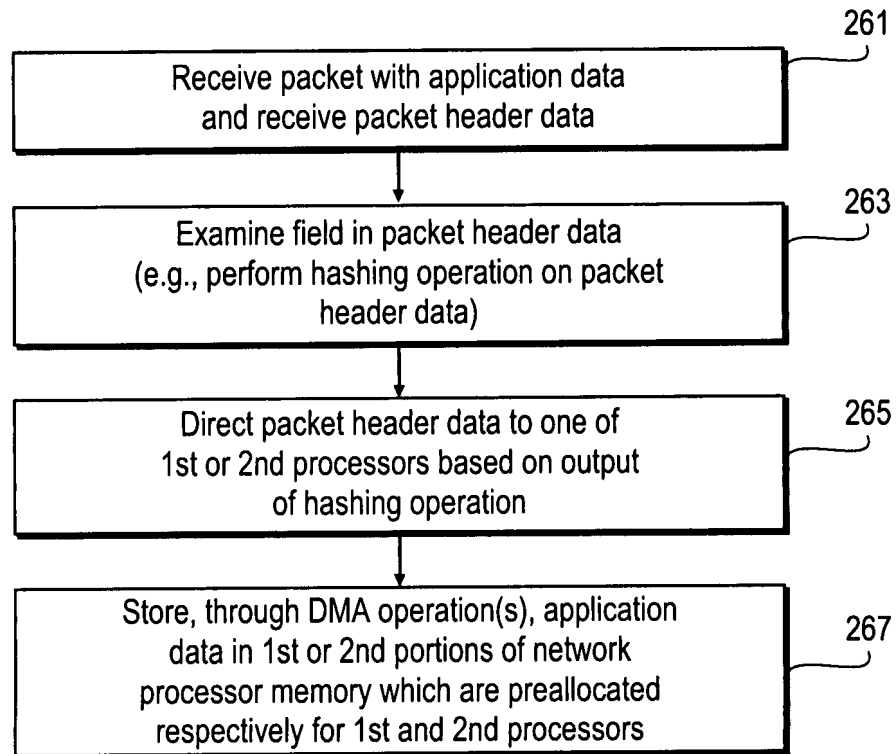


FIG. 8C

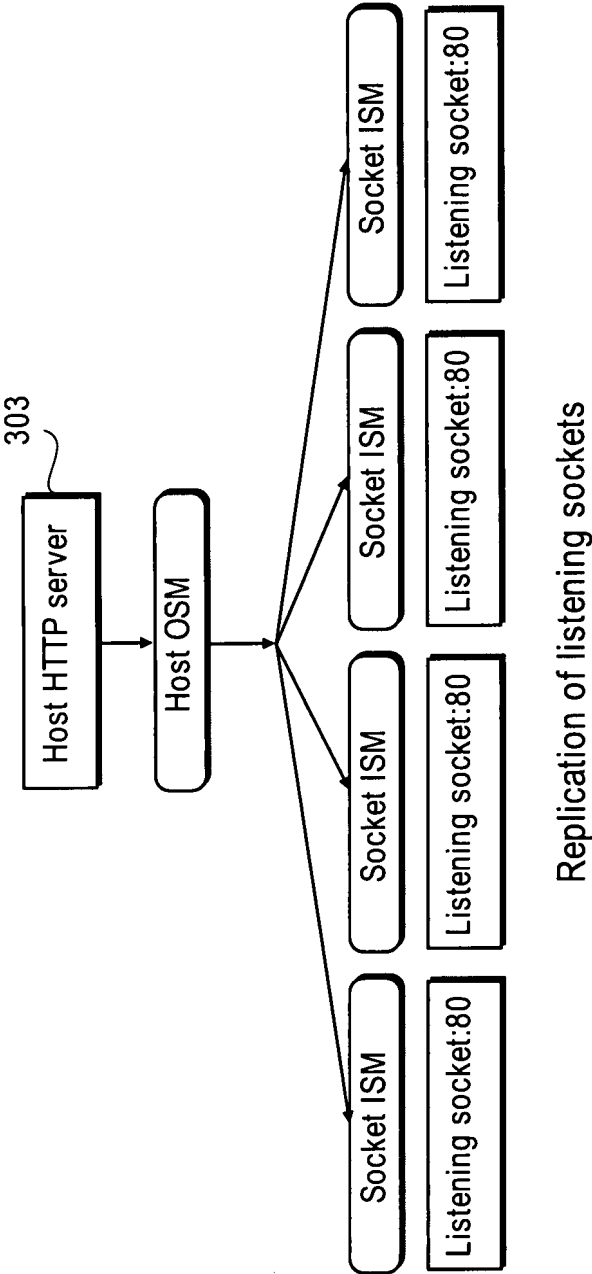


FIG. 9A

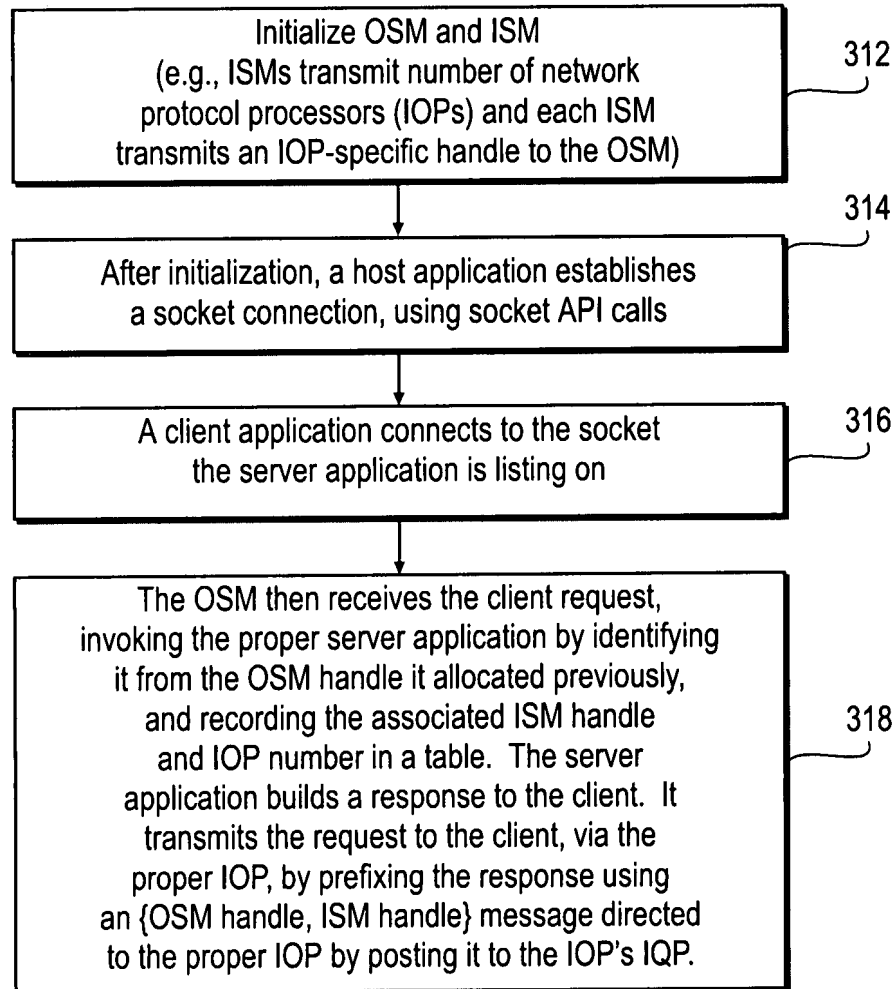


FIG. 9B

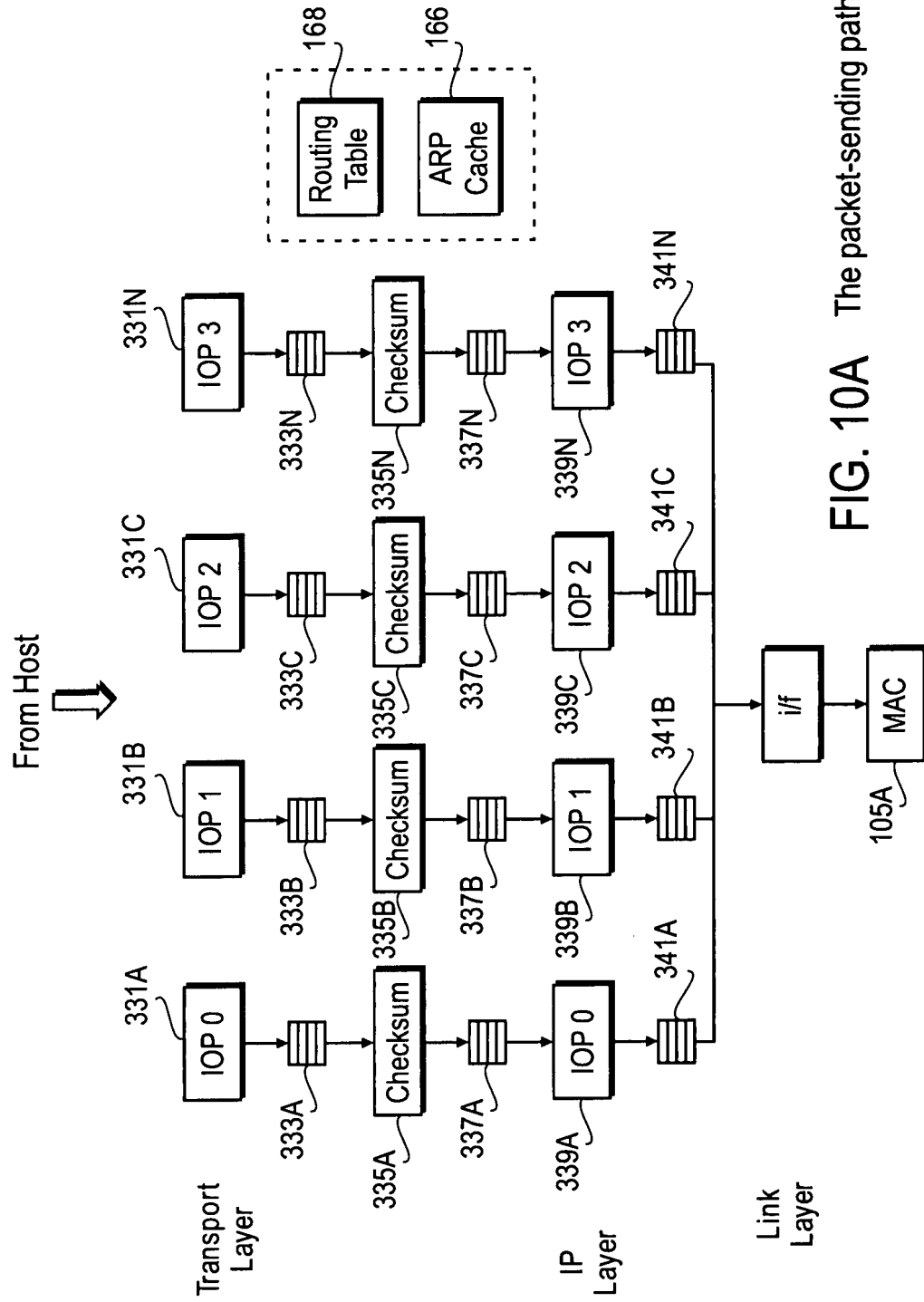


FIG. 10A The packet-sending path

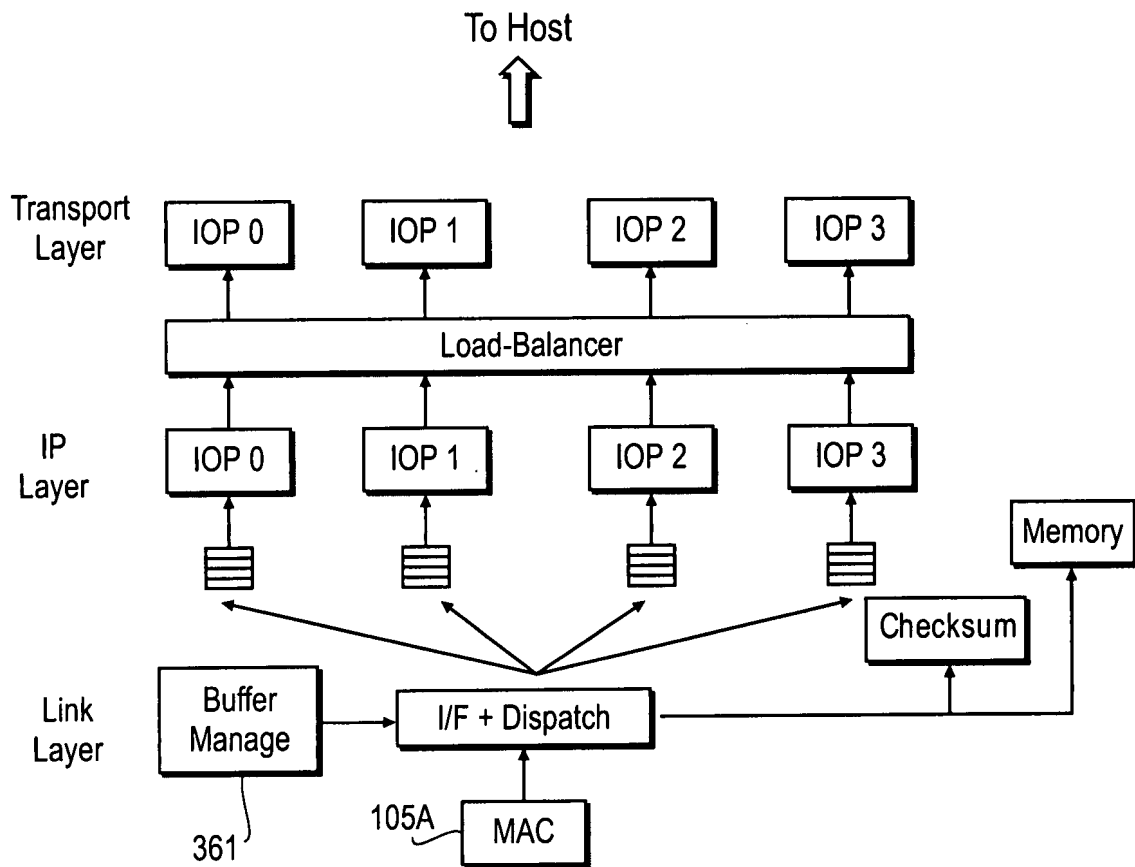


FIG. 10B The packet-receiving path

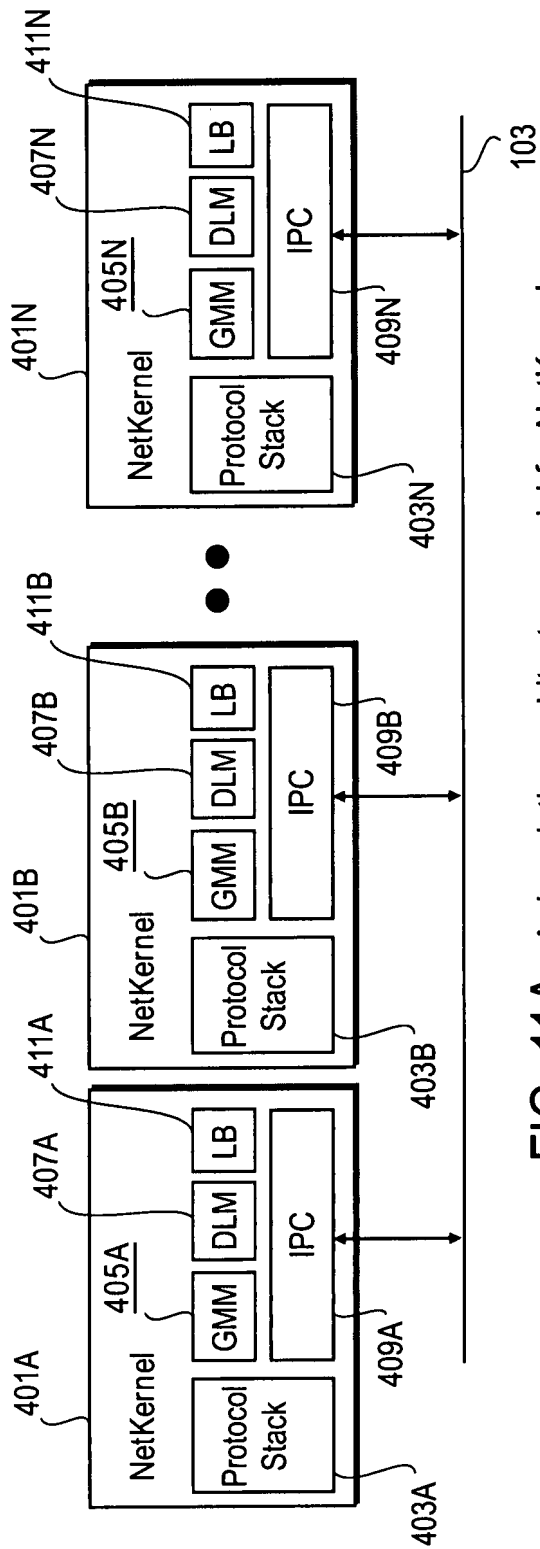


FIG. 11A A descriptive architecture model for NetKernel

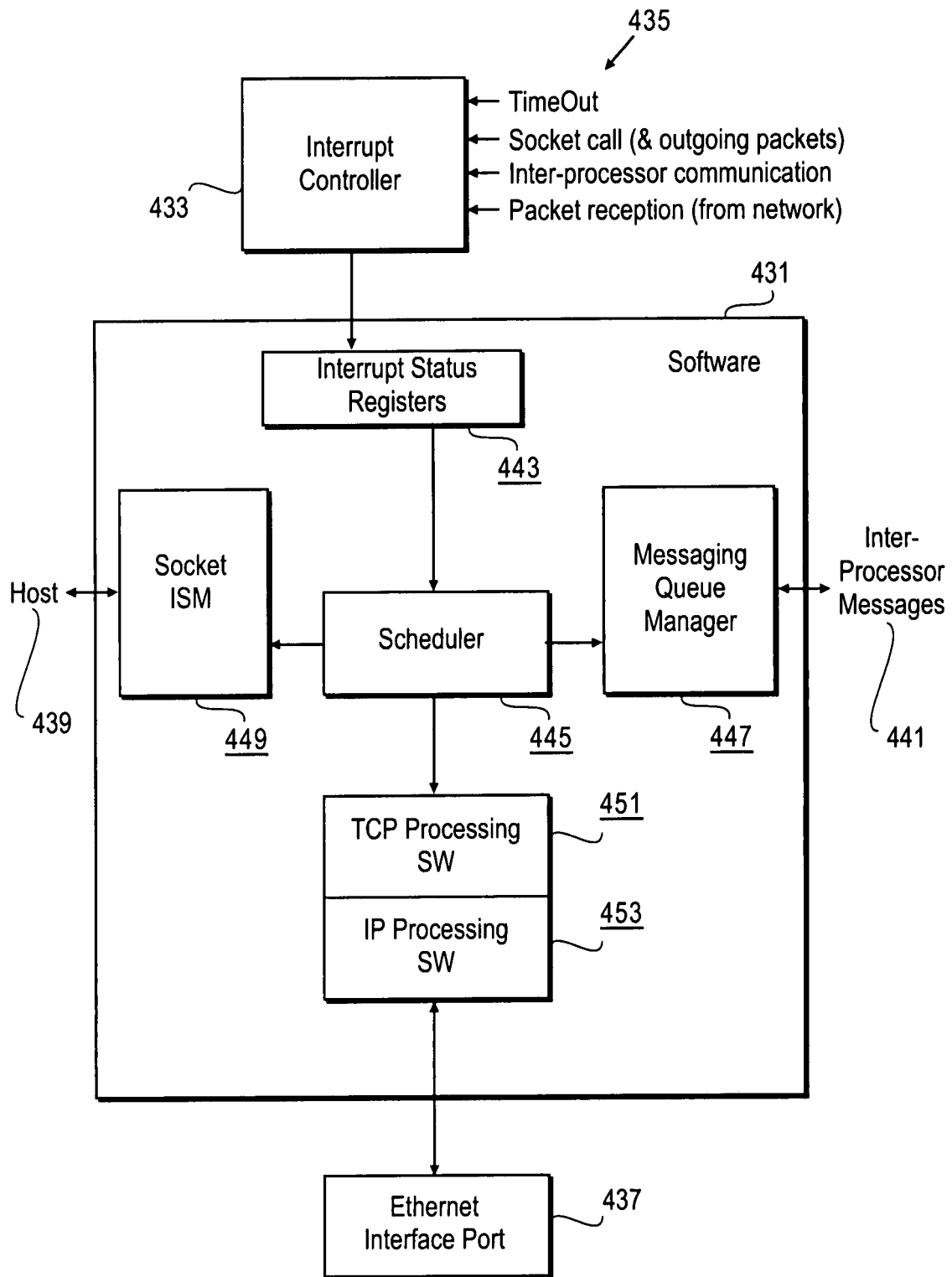


FIG. 11B

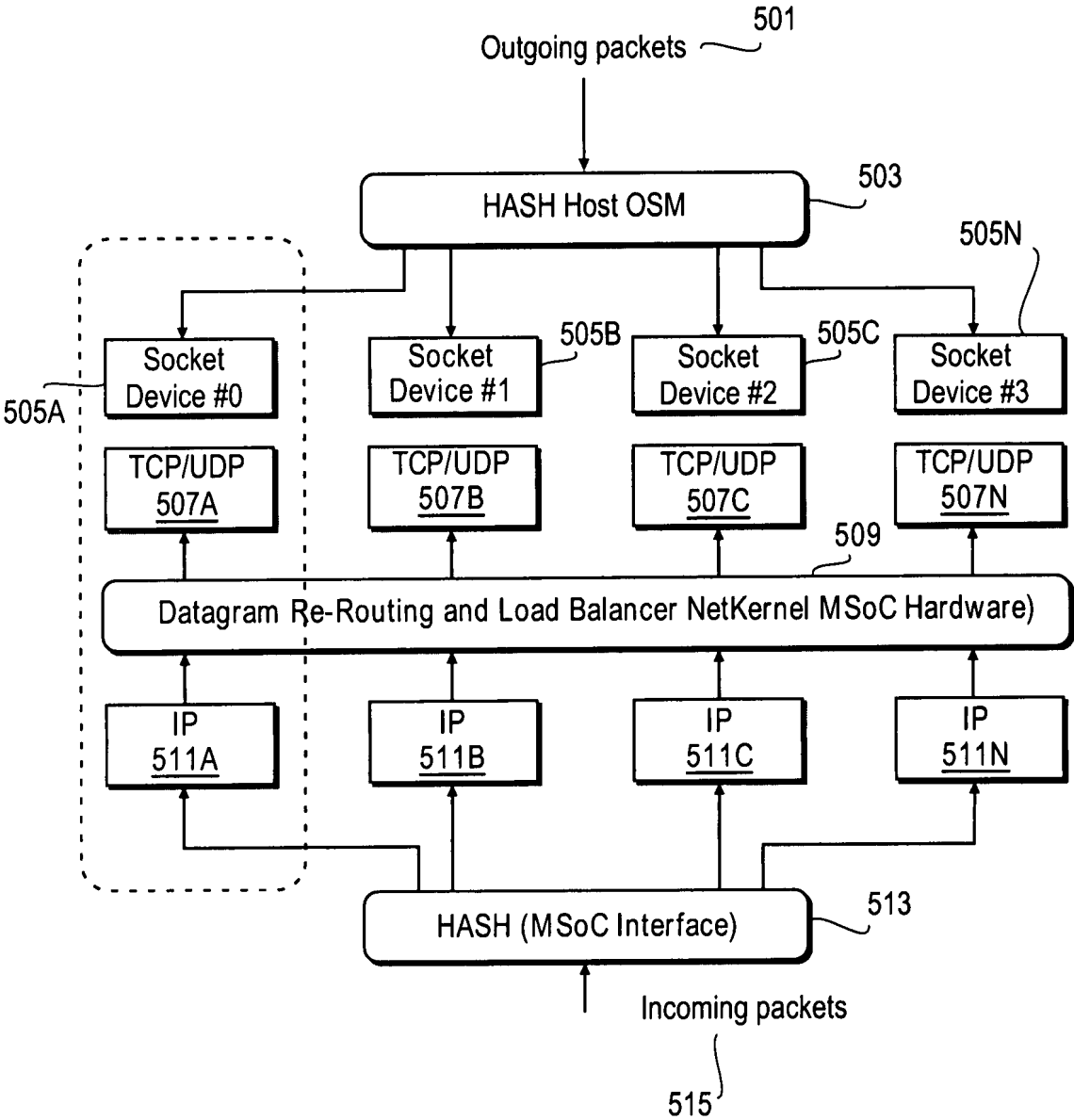


FIG. 12A

An illustration of packet flows for load balancing

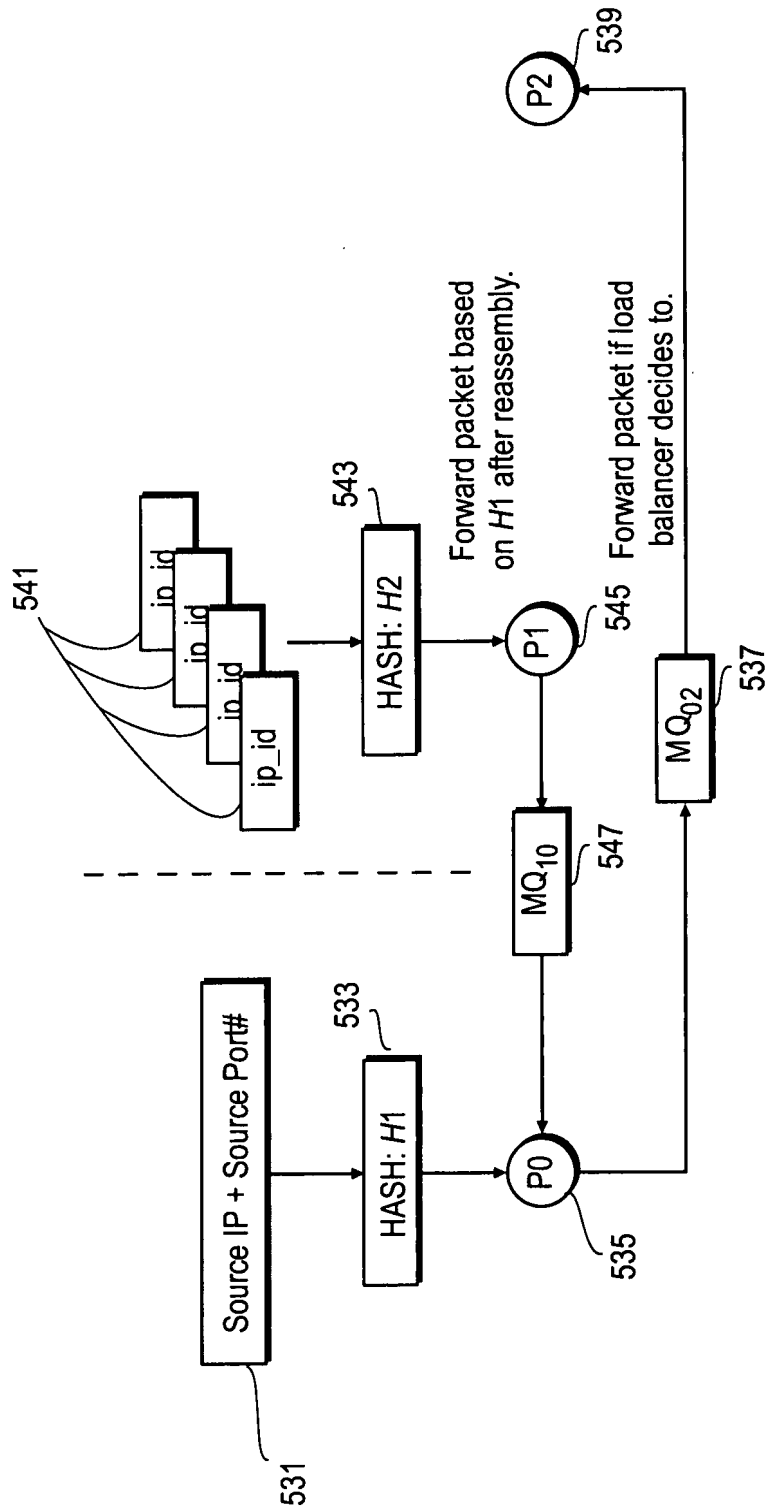


FIG. 12B Illustration of directing packets on address hashing and load balancing

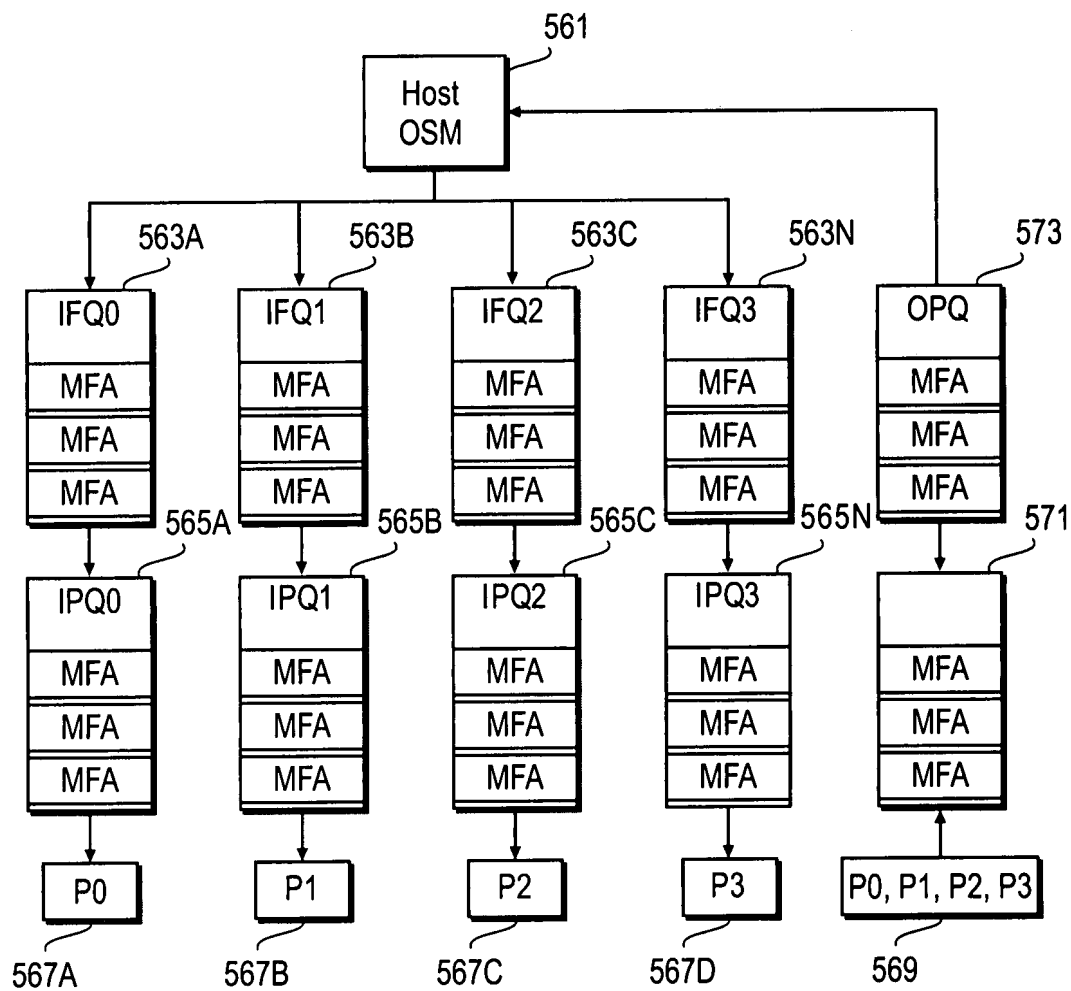


FIG. 13 I2O free and post queues in MSoC

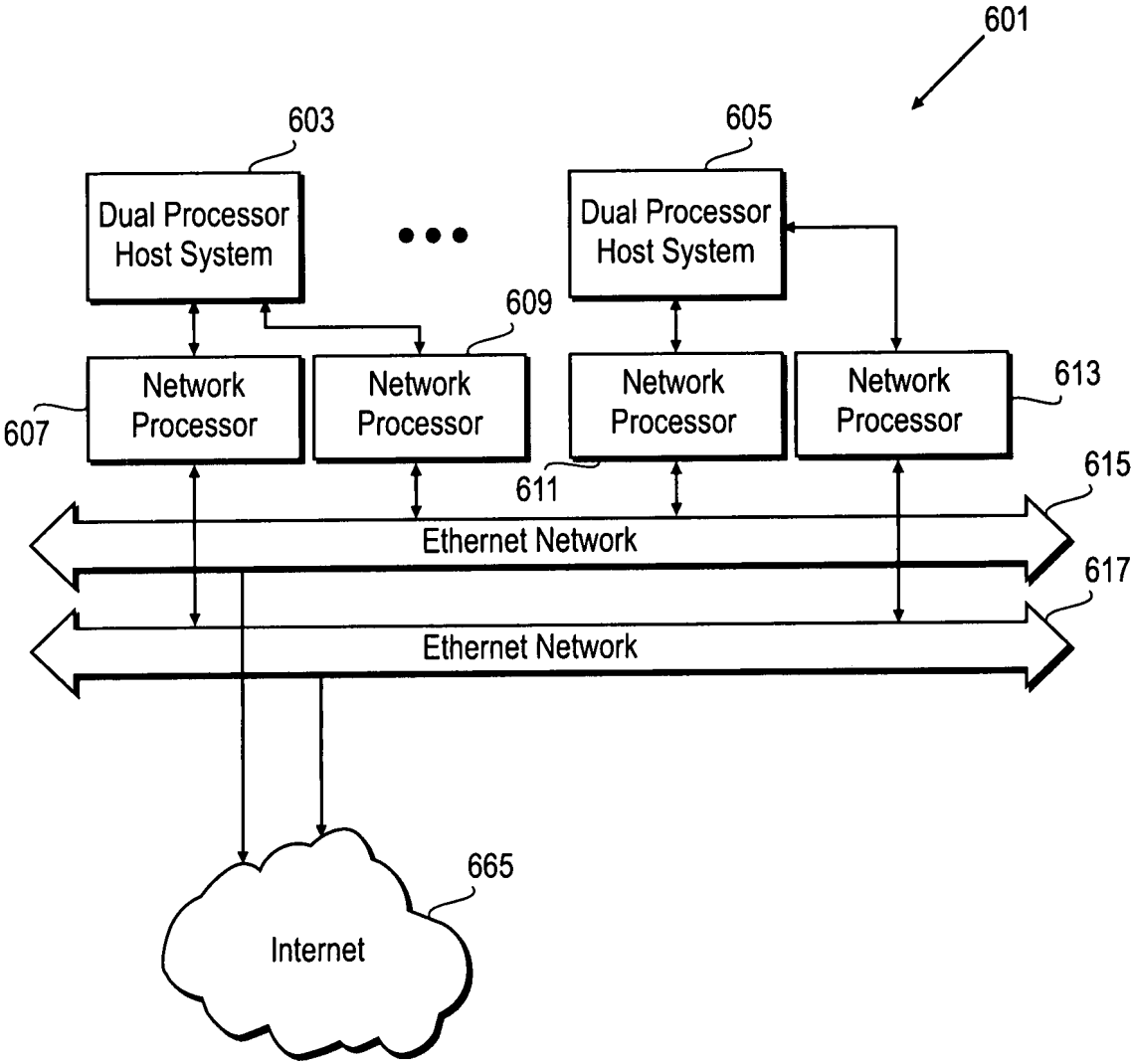


FIG. 14

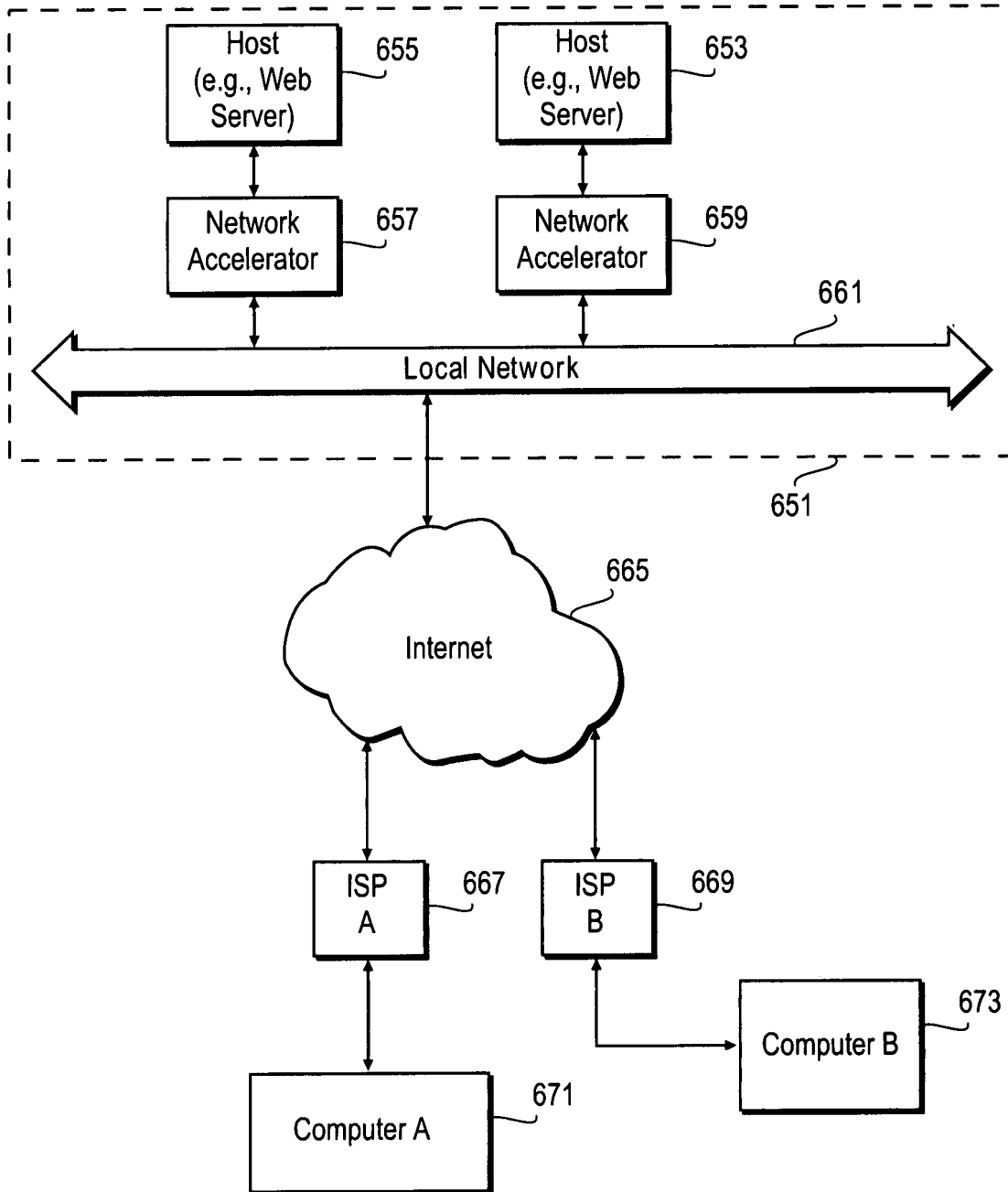


FIG. 15

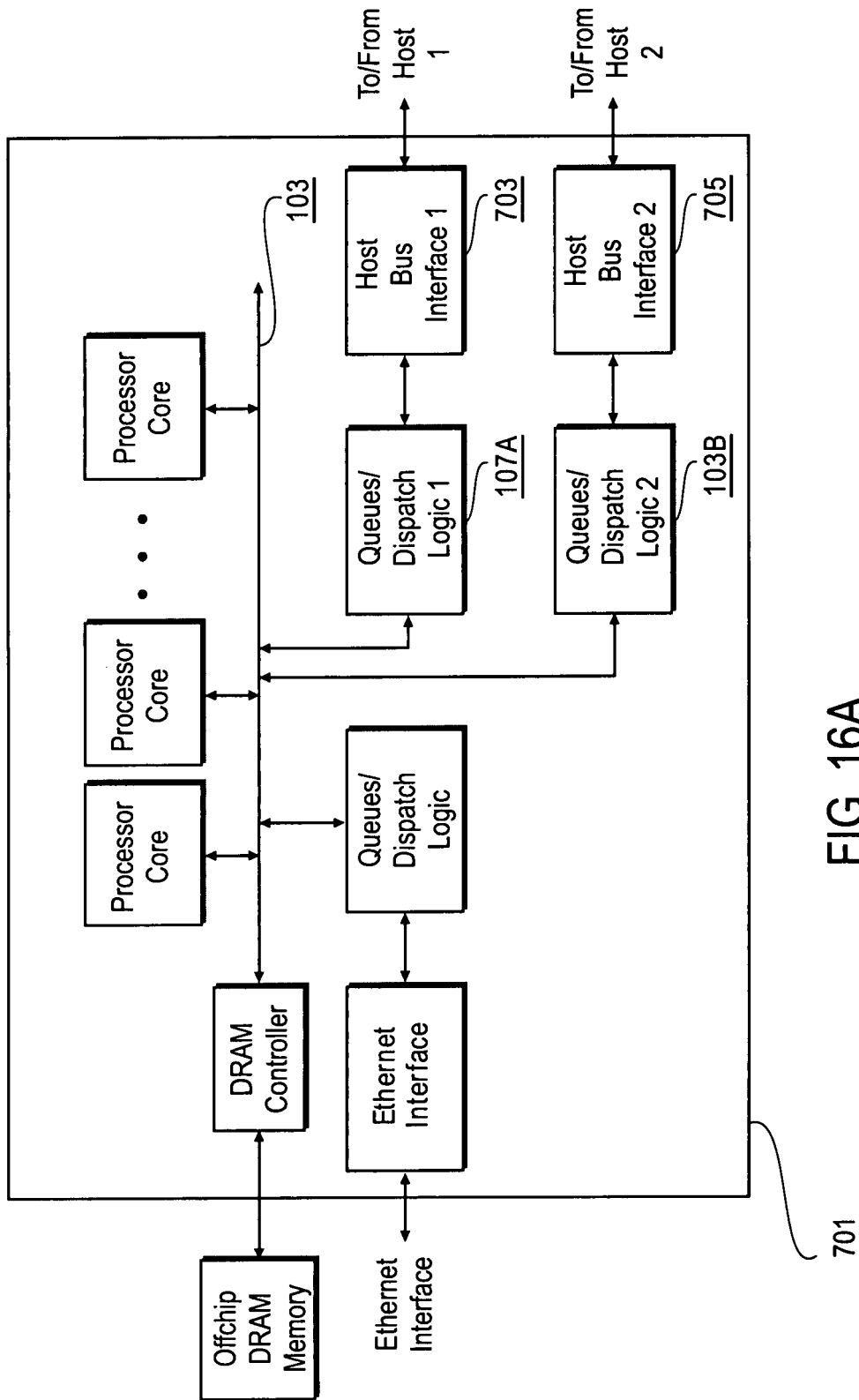
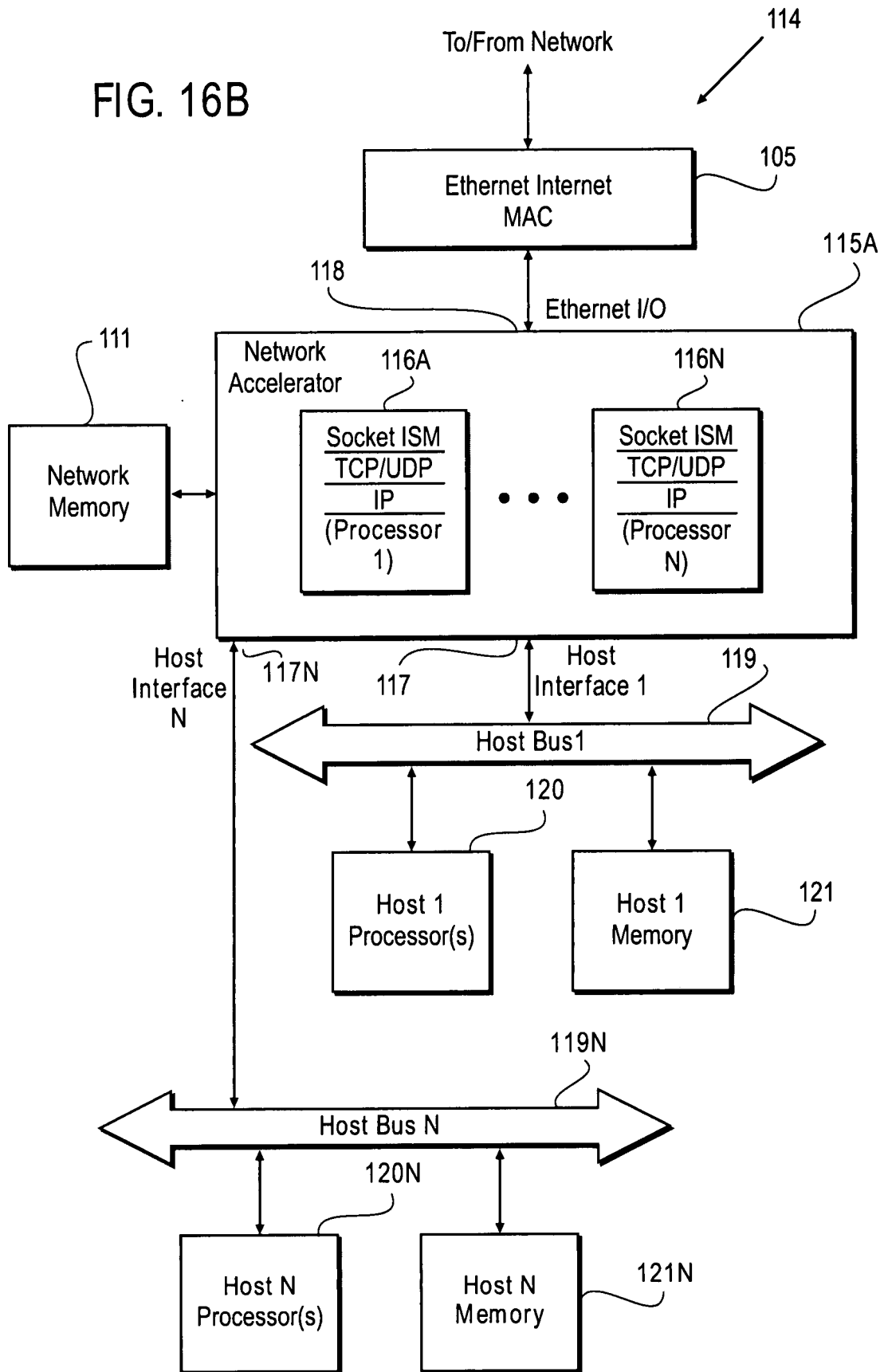


FIG. 16A

FIG. 16B



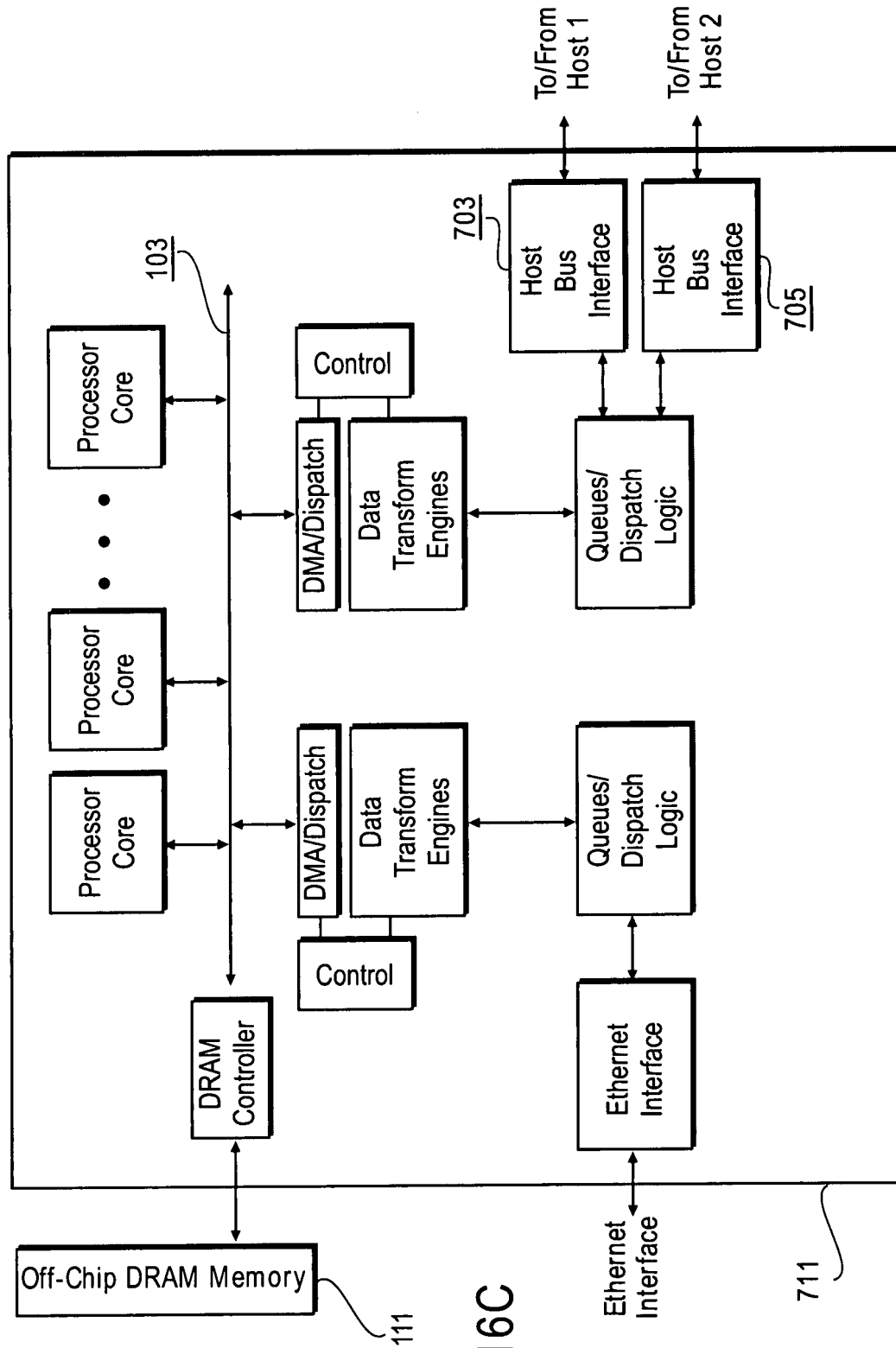


FIG. 16C

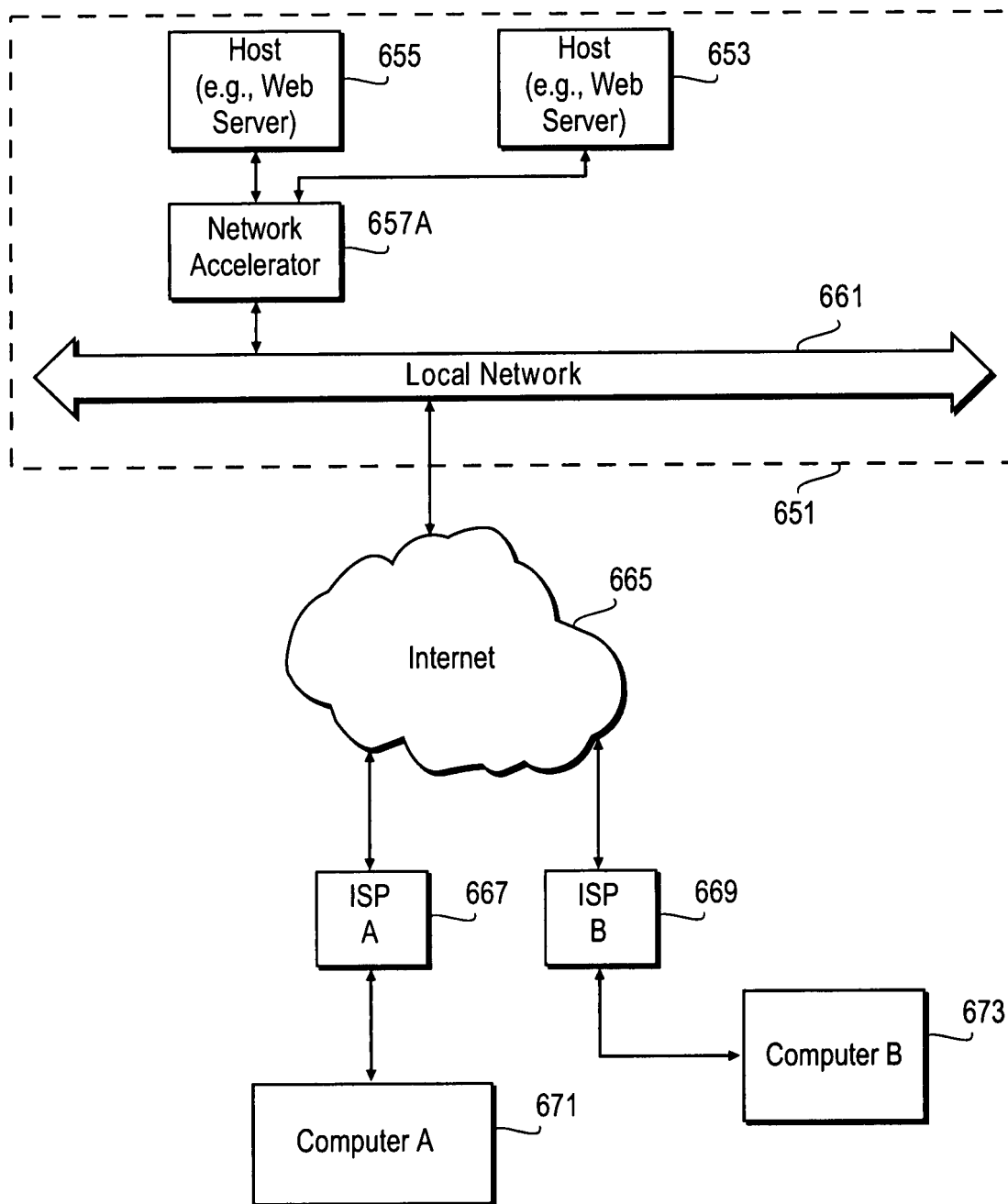


FIG. 16D

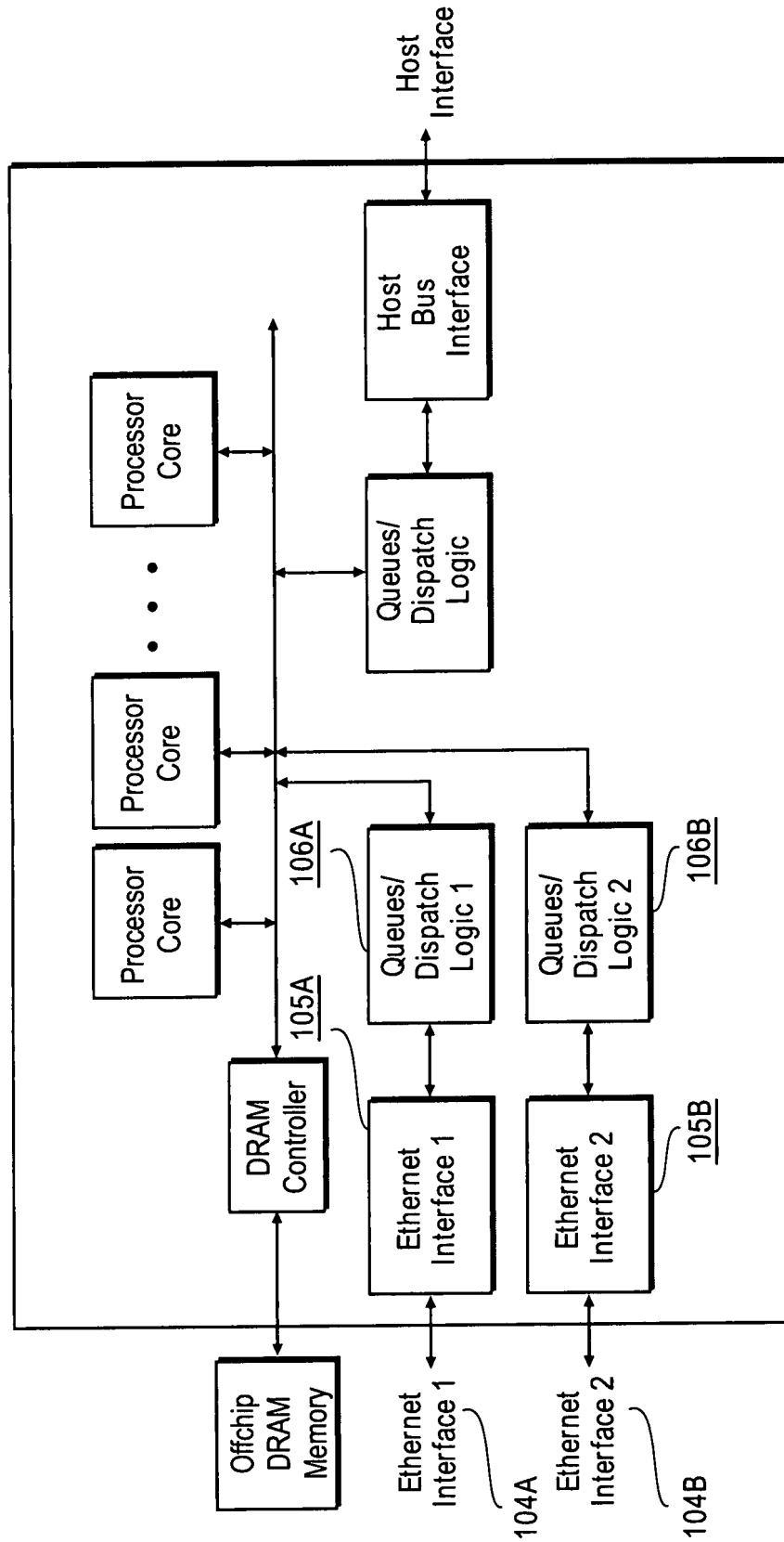


FIG. 17A

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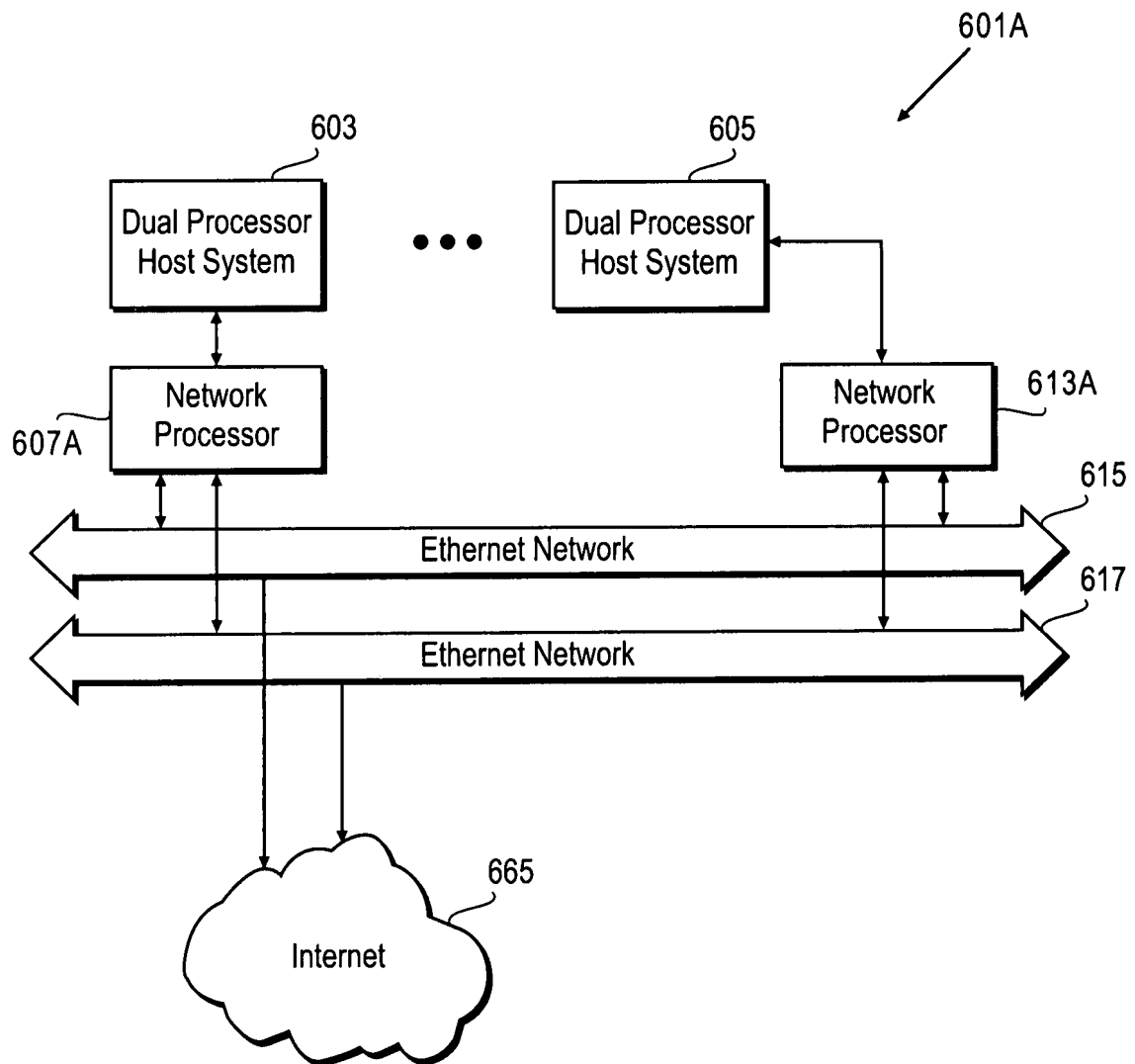


FIG. 17B

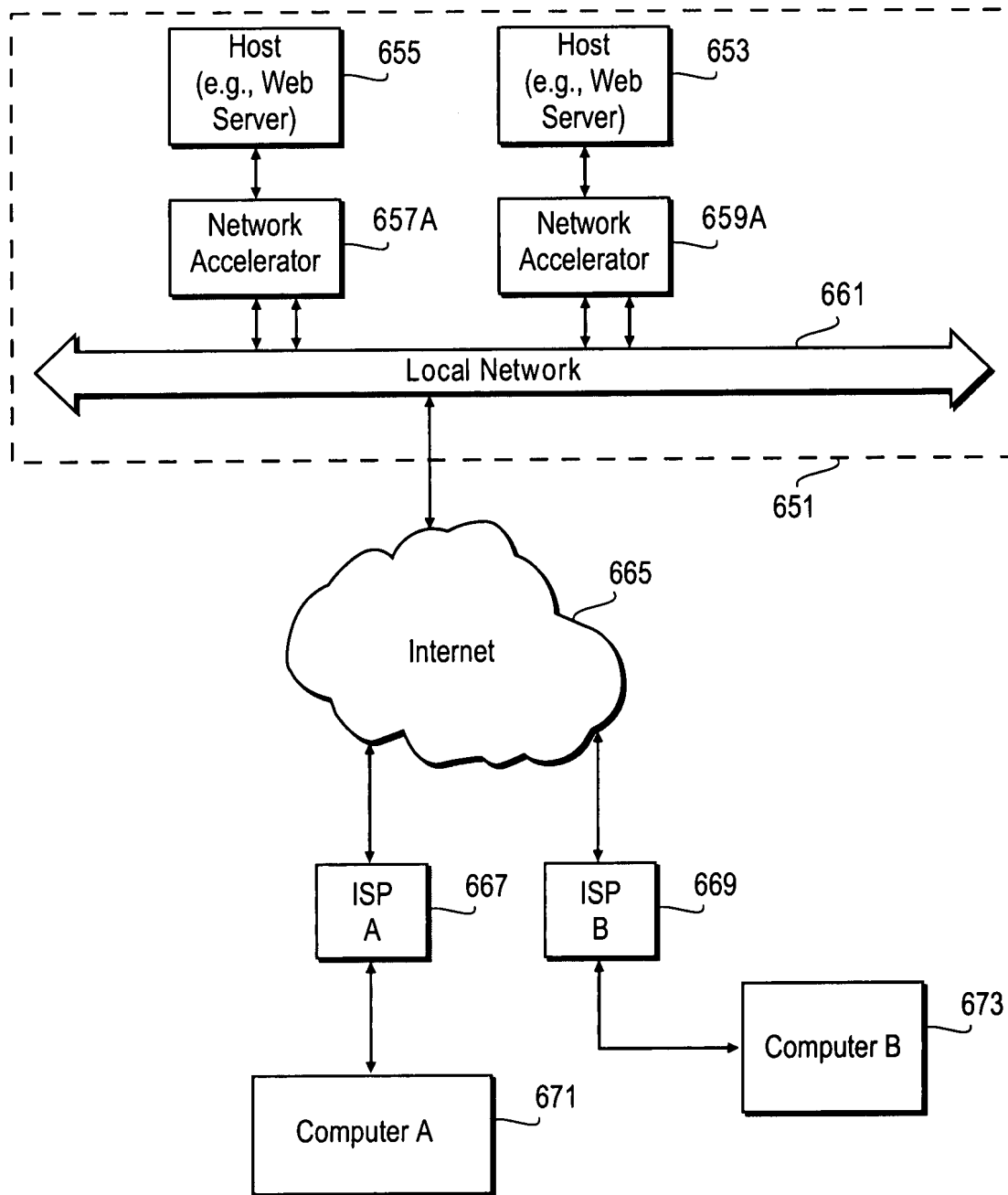


FIG. 17C